I claim:

1. An apparatus, comprising:

a test controller, coupled between a bus and first and second cores in a multi-core computer, to operate a data flow stress test in the multi-core computer.

2. The apparatus of claim 1, wherein the test controller includes:

a first multiplexer coupled between the first core and the bus;

a second multiplexer coupled between the second core and the bus;

a register to store test information; and

a control circuit coupled between the register and the first and second multiplexers.

3. The apparatus of claim 2, wherein:

the control circuit is to select an input of the first multiplexer between a first data request from the first core and a test data request from the control circuit.

4. The apparatus of claim 2, wherein:

the control circuit is to select an input of the second multiplexer between a first data request from the second core and a test data request from the control circuit.

5. The apparatus of claim 1, wherein:
the test controller is to be electrically deactivated after the stress test.

6. The apparatus of claim 1, wherein:
the test controller, first core, and second core are all disposed on a same integrated circuit.

7. A system comprising:
a memory;
a multi-core computer coupled to the memory; and
a test controller, coupled between a bus and first and second cores in the multi-core computer, to operate a data flow stress test in the multi-core computer.

8. The system of claim 7, wherein the test controller includes:
switching logic to switch the bus between data requests from at least one of the first and second cores and test data requests from the test controller.

9. The system of claim 7, wherein:
the test controller includes a register to store test information.

10. The system of claim 7, wherein:
the test controller is to be electrically deactivated after the stress test.

11. The system of claim 7, wherein:

the test controller, first core, and second core are all disposed on a same integrated circuit.

12. A method comprising:

stress testing a multi-core computer by blocking a first data request from a first core to a bus and sending a test data request from a test controller to the bus.

13. The method of claim 12, wherein:

the stress testing further includes sending a second data request from a second core to the bus.

14. The method of claim 12, wherein:

the blocking and the sending are controlled by a test controller.

15. The method of claim 12, wherein:

the test data request is selected by a test register.

16. The method of claim 12, wherein:

the stress testing further includes blocking a second data request from a second core to the bus.

17. A machine-readable medium that provides instructions, which when executed by a set of one or more processors, cause said set of processors to perform operations comprising:

stress testing a multi-core computer by blocking a first data request from a first core to a bus and sending a test data request from a test controller to the bus.

18. The medium of claim 17, wherein:

the stress testing further includes sending a second data request from a second core to the bus.

19. The medium of claim 17, wherein:

the blocking and the sending are controlled by a test controller.

20. The medium of claim 17, wherein:

the test data request is selected by a test register.

21. The medium of claim 17, wherein:

stress testing further includes blocking a second data request from a second core to the bus.

22. A machine-readable medium that provides instructions, which when executed by a set of one or more processors, cause said set of processors to perform operations comprising:

obtaining test data for stress testing a multi-core computer system;

writing the test data to a test register in the multi-core computer system through a test port; and

reading a test result from the test register through the test port.

23. The medium of claim 22, wherein:

said writing includes writing an ‘on’ bit to the test register.

25. The medium of claim 22, wherein:

said writing includes specifying data requests to be monitored as triggers to start a test.

26. The medium of claim 22, wherein:

said writing includes specifying test data requests to be issued.